

## CC-PC Glue Board J5 Connector

Pin	Name	Class	Direction	Comment	Pin	Name	Class	Direction	Comment
A1	VCC(5V)	POWER	in		B1	GND	POWER		
A2	GND	POWER			B2	LAD16	PLX	in/out	
A3	LAD0	PLX	in/out		B3	LAD17	PLX	in/out	
A4	LAD1	PLX	in/out		B4	LAD18	PLX	in/out	
A5	LAD2	PLX	in/out		B5	LAD19	PLX	in/out	
A6	LAD3	PLX	in/out		B6	LAD20	PLX	in/out	
A7	LAD4	PLX	in/out		B7	LAD21	PLX	in/out	
A8	LAD5	PLX	in/out		B8	LAD22	PLX	in/out	
A9	LAD6	PLX	in/out		B9	LAD23	PLX	in/out	
A10	LAD7	PLX	in/out		B10	GND	POWER		
A11	GND	POWER			B11	LAD24	PLX	in/out	
A12	LAD8	PLX	in/out		B12	LAD25	PLX	in/out	
A13	LAD9	PLX	in/out		B13	LAD26	PLX	in/out	
A14	LAD10	PLX	in/out		B14	LAD27	PLX	in/out	
A15	LAD11	PLX	in/out		B15	LAD28	PLX	in/out	
A16	LAD12	PLX	in/out		B16	LAD29	PLX	in/out	
A17	LAD13	PLX	in/out		B17	LAD30	PLX	in/out	
A18	LAD14	PLX	in/out		B18	LAD31	PLX	in/out	
A19	LAD15	PLX	in/out		B19	GND	POWER		
A20	GND	POWER			B20	NC		Reserved	
A21	ADS#	PLX	out		B21	NC		Reserved	
A22	LW/R#	PLX	out		B22	NC		Reserved	
A23	ALE	PLX	out		B23	NC		Reserved	
A24	NC		Reserved		B24	NC		Reserved	
A25	READYi#	PLX	in		B25	GND	POWER		
A26	CS0#	PLX	out	Chip Select Address Space 0 (used internally)	B26	TCK1	JTAG	out	
A27	CS1#	PLX	out	Chip Select Address Space 1	B27	TDO1	JTAG	in	
A28	NC		Reserved		B28	TMS1	JTAG	out	
A29	GND	POWER			B29	TDI1	JTAG	out	
A30	LRESETo#	PLX	out	Asserted with PCI RST (don't use)	B30	TRES1	JTAG	out	
A31	BLAST#	PLX	out		B31	GND	POWER		
A32	LCLK	PLX	in		B32	TCK2	JTAG	out	
A33	LINTi1	PLX	in		B33	TDO2	JTAG	in	
A34	GND	POWER			B34	TMS2	JTAG	out	
A35	LINTi2	PLX	in		B35	TDI2	JTAG	out	
A36	LA6	PLX	out	Local Bus Address	B36	TRES2	JTAG	out	
A37	LBE0#	PLX	out		B37	GND	POWER		
A38	LBE1#	PLX	out		B38	GND	POWER		
A39	LBE2#	PLX	out		B39	GND	POWER		
A40	LBE3#	PLX	out		B40	GND	POWER		
A41	GND	POWER			B41	NC		Reserved	
A42	LA7	PLX	out	Local Bus Address	B42	NC		Reserved	
A43	LA8	PLX	out	Local Bus Address	B43	GND	POWER		
A44	LREQ	PLX	in	Local Bus Request	B44	GND	POWER		
A45	LGNT	PLX	out	Local Bus Grant	B45	VCC(5V)	POWER	in	
A46	LA11	PLX	out	Local Bus Address	B46	I2C1D	I2C	in/out	External Pull-ups
A47	LA12	PLX	out	Local Bus Address	B47	I2C1C	I2C	out	External Pull-ups
A48	GND	POWER			B48	GND	POWER		
A49	GPIO4	PLX	in/out	or LA27	B49	I2C2D	I2C	in/out	External Pull-ups
A50	GPIO5	PLX	in/out	or LA26	B50	I2C2C	I2C	out	External Pull-ups
A51	GPIO6	PLX	in/out	or LA25	B51	GND	POWER		
A52	GPIO7	PLX	in/out	or LA24	B52	I2C3D	I2C	in/out	External Pull-ups
A53	GPIO8	PLX	in/out		B53	I2C3C	I2C	out	External Pull-ups
A54	LA18	PLX	out	Local Bus Address	B54	GND	POWER		
A55	GND	POWER			B55	I2C4D	I2C	in/out	External Pull-ups
A56	LA19	PLX	out	Local Bus Address	B56	I2C4C	I2C	out	External Pull-ups
A57	LA20	PLX	out	Local Bus Address	B57	GND	POWER		
A58	RD#	PLX	out	Read Strobe (active Low)	B58	GND	POWER		
A59	WR#	PLX	out	Write Strobe (Active Low)	B59	LA21	PLX	out	Local Bus Address
A60	LA23	PLX	out	Local Bus Address	B60	LA22	PLX	out	Local Bus Address

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Pin	Name	Class	Direction	Comment	Pin	Name	Class	Direction	Comment
A61	GND	POWER			B61	DCDV1	RS232	in	
A62	NC		Reserved		B62	RXDV1	RS232	in	
A63	NC		Reserved		B63	TXDV1	RS232	out	
A64	NC		Reserved		B64	DTRV1	RS232	out	
A65	NC		Reserved		B65	GND	RS232		
A66	GND	POWER			B66	DSRV1	RS232	in	
A67	VCC(5V)	POWER	in		B67	RTSV1	RS232	out	
A68	NC		Reserved		B68	CTSV1	RS232	in	
A69	GND	POWER			B69	RTV1	RS232	in	
A70	Reseved	LHCb	in		B70	GND	POWER		
A71	Reserved	LHCb	in		B71	NC		Reserved	
A72	Reserved	LHCb	in		B72	NC		Reserved	
A73	Reserved	LHCb	in		B73	NC		Reserved	
A74	Reserved	LHCb	in		B74	NC		Reserved	
A75	Reserved	LHCb	in		B75	NC		Reserved	
A76	Reserved	LHCb	in		B76	GND	POWER		
A77	Reserved	LHCb	in		B77	NC		Reserved	
A78	Reserved	LHCb	out		B78	NC		Reserved	
A79	Reserved	LHCb	out		B79	NC		Reserved	
A80	Reserved	LHCb	out		B80	NC		Reserved	
A81	Reserved	LHCb	out		B81	NC		Reserved	
A82	Reserved	LHCb	out		B82	NC		Reserved	
A83	Reserved	LHCb	out		B83	NC		Reserved	
A84	Reserved	LHCb	out		B84	NC		Reserved	
A85	Reserved	LHCb	out		B85	GND	POWER		
A86	GND	POWER			B86	NC		Reserved	
A87	LA2	PLX	out	Local Bus Address	B87	NC		Reserved	
A88	LA3	PLX	out	Local Bus Address	B88	NC		Reserved	
A89	LA4	PLX	out	Local Bus Address	B89	NC		Reserved	
A90	GND	POWER			B90	VCC(5V)	POWER	in	
A91	3.3V	POWER	in		B91	3.3V	POWER	in	
A92	GND	POWER			B92	VCC(5V)	POWER	in	
A93	GND	POWER			B93	GND	POWER		
A94	GPIO0	PLX	in/out	or WAITo#	B94	VCC(5V)	POWER	in	
A95	LA5	PLX	out	Local Bus Address	B95	GND	POWER		
A96	LA9	PLX	out	Local Bus Address	B96	VCC(5V)	POWER	in	
A97	LA10	PLX	out	Local Bus Address	B97	GND	POWER		
A98	LA13	PLX	out	Local Bus Address	B98	RESET#	CORE	in	3.3V Active Low
A99	NC		Reserved		B99	GND	POWER		
A100	NC		Reserved		B100	9		Reserved	
A101	VCC(5V)	POWER	in		B101	GND	POWER		
A102	GND	POWER			B102	GND	POWER		
A103	GND	POWER			B103	GND	POWER		
A104	VCC(5V)	POWER	in		B104	3.3V	POWER	in	
A105	GPIO1	PLX	in/out	or LLOCKo#	B105	3.3V	POWER	in	
A106	GPIO2	PLX	in/out	or CS2#	B106	3.3V	POWER	in	
A107	GPIO3	PLX	in/out	or CS3#	B107	3.3V	POWER	in	
A108	BTERM#	PLX	in		B108	3.3V	POWER	in	
A109	LA14	PLX	out	Local Bus Address	B109	3.3V	POWER	in	
A110	LA15	PLX	out	Local Bus Address	B110	3.3V	POWER	in	
A111	LA16	PLX	out	Local Bus Address	B111	NC		Reserved	
A112	GND	POWER			B112	GND	POWER		
A113	GND	POWER			B113	VCC(5V)	POWER	in	
A114	GND	POWER			B114	GND	POWER		
A115	LA17	PLX	out	Local Bus Address	B115	TCK3	JTAG	out	
A116	BCLKo	PLX	out	Buffered PCI clock	B116	TDO3	JTAG	in	
A117	JPWR1	JTAG	in	JTAG1 Signaling Level	B117	TMS3	JTAG	out	
A118	JPWR2	JTAG	in	JTAG2 Signaling Level	B118	TDI3	JTAG	out	
A119	JPWR3	JTAG	in	JTAG3 Signaling Level	B119	TRES3	JTAG	out	
A120	GND	POWER			B120	GND	POWER		